

190

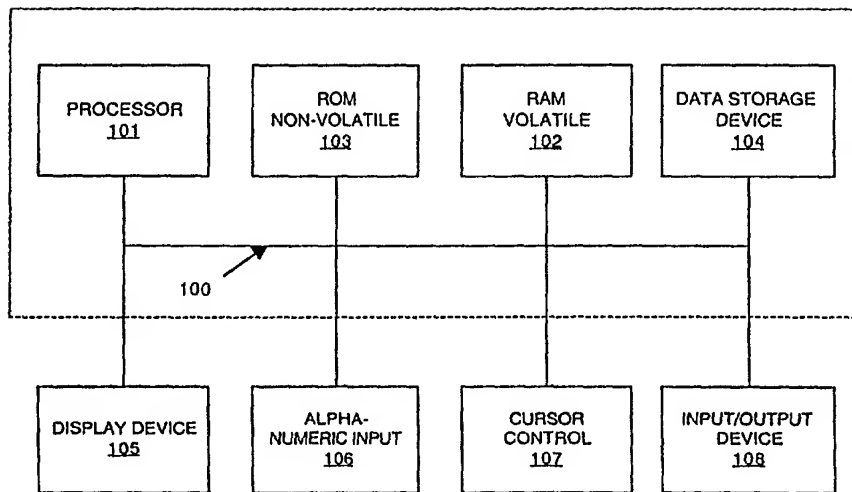


Figure 1

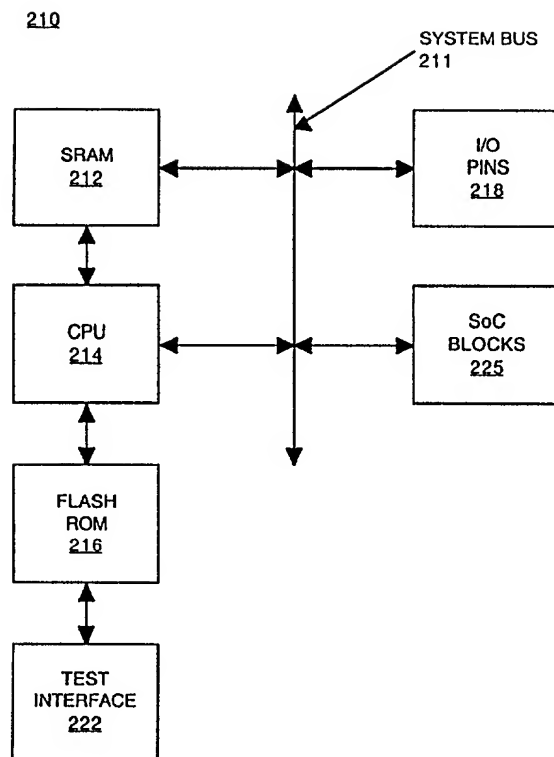


Figure 2A

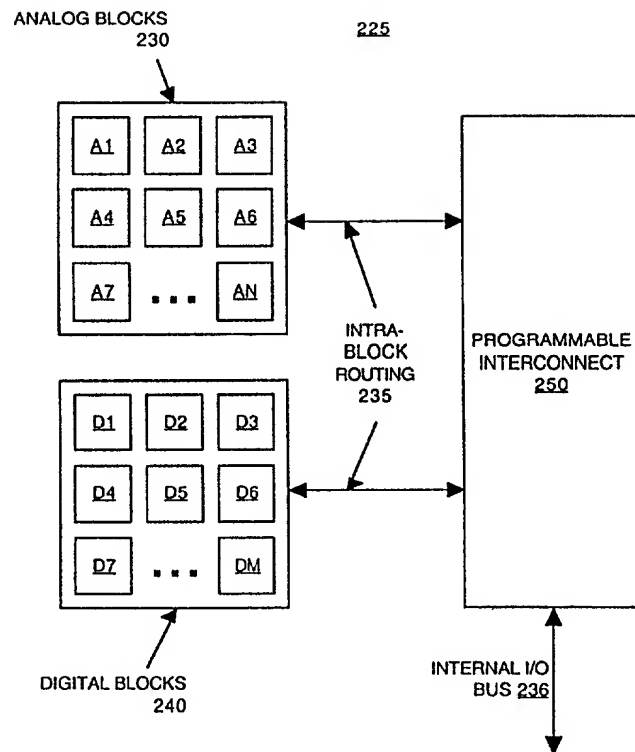


Figure 2B

300

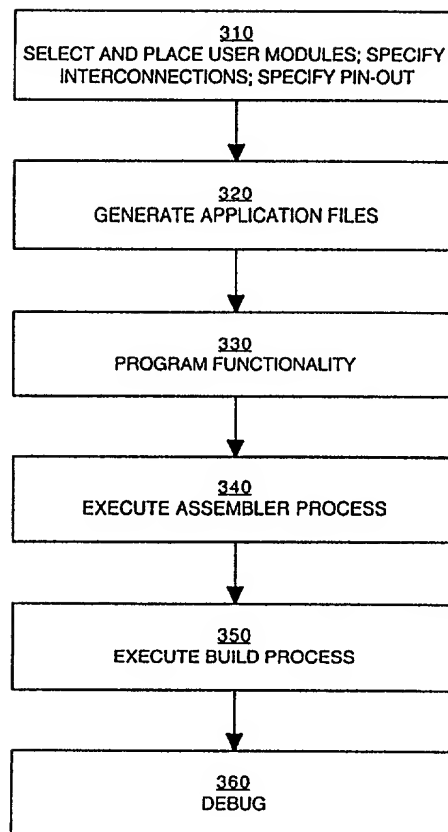


Figure 3

400 401 402 403 404 405 406 407 408 409 410 411 412 413 414 415 416 417 418 419 420 421 422 423 424 425 426 427 428 429 430 431 432 433 434 435 436 437 438 439 440 441 442 443 444 445 446 447 448 449 450 451 452 453 454 455 456 457 458 459 460 461 462 463 464 465 466 467 468 469 470 471 472 473 474 475 476 477 478 479 480 481 482 483 484 485 486 487 488 489 490 491 492 493 494 495 496 497 498 499 500

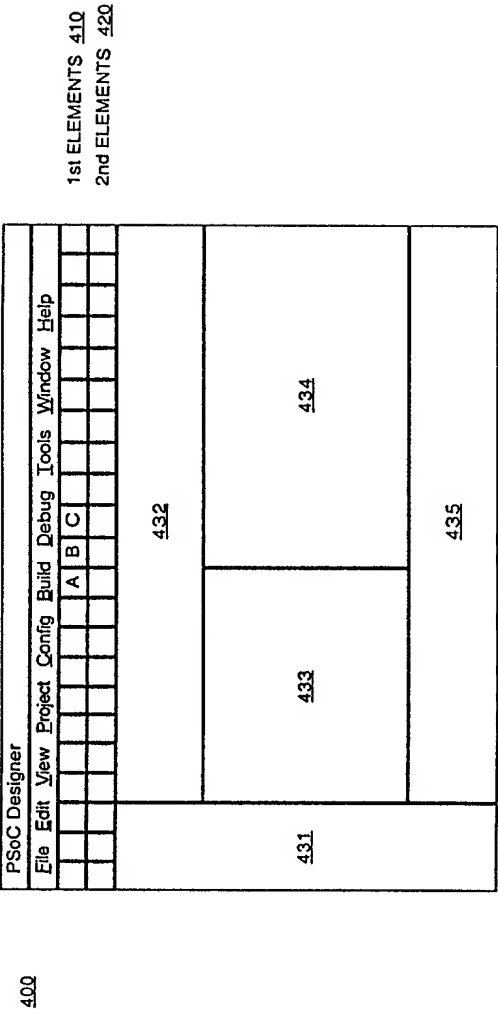


Figure 4A

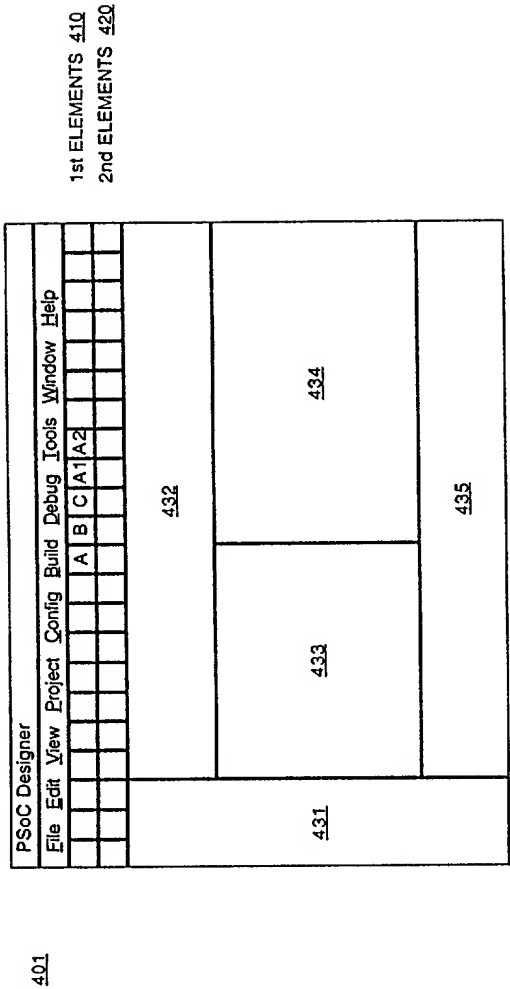


Figure 4B

1st ELEMENTS 410
2nd ELEMENTS 420

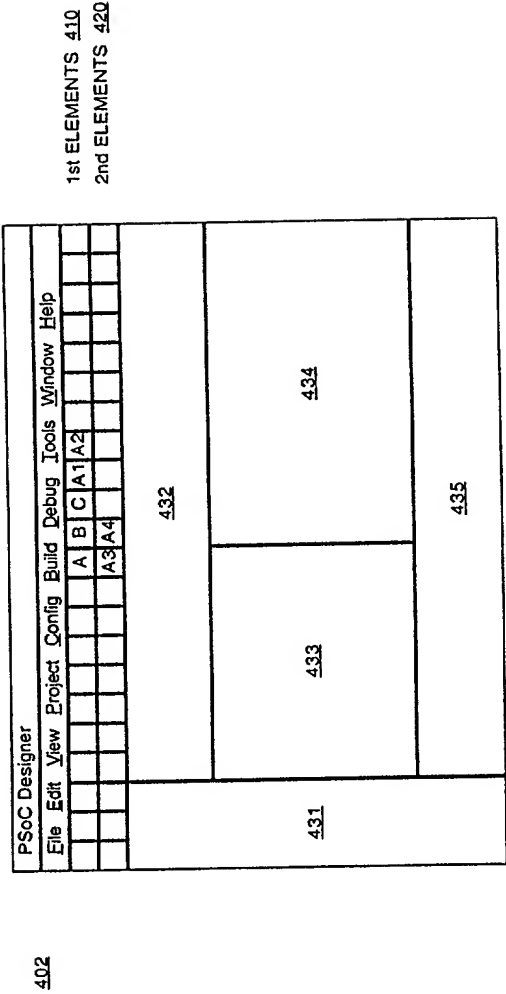
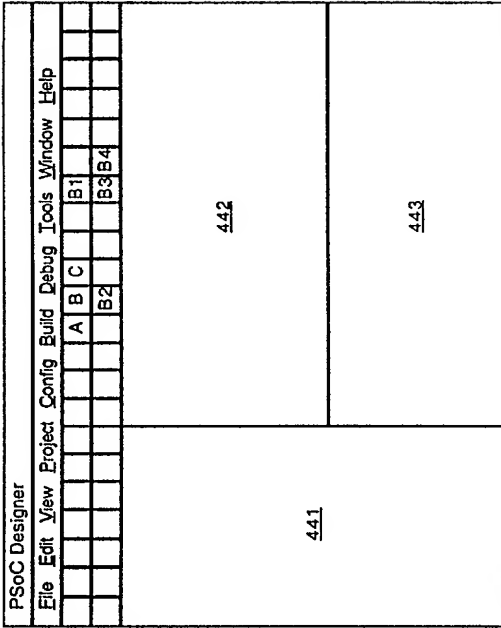


Figure 4C

PSoC Designer



403

1st ELEMENTS 410
2nd ELEMENTS 420

Figure 4D

500

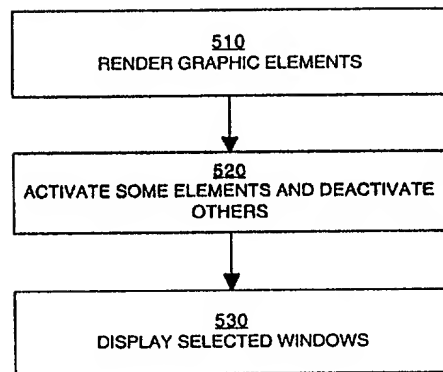


Figure 5

600

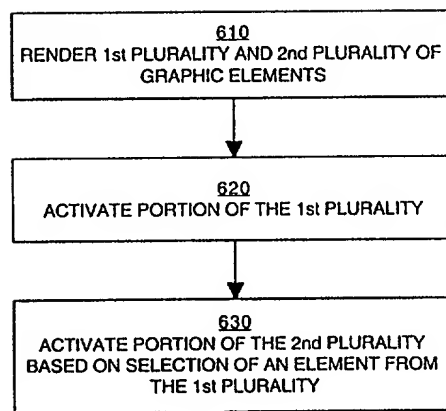


Figure 6

newproject - PSoC Designer - [Device Editor]

File Edit View Project Config Build Debug Program Tools Window Help

Use Modules selected for placement:

ADCs: AD_CONVERT_1, COMP_1, DAC_1, HSAMP_1, HSAMP_2, PWN16_1, UART_1

PinMux: P0_16, P0_15, P0_14, P0_13, P0_12, P0_11, P0_10, P0_9, P0_8, P0_7, P0_6, P0_5, P0_4, P0_3, P0_2, P0_1, P0_0

Diagram showing a circuit with a P0_16 pin connected to a Counter, which is connected to a Comparator. The Comparator is connected to a P0_15 pin. The Counter is also connected to a P0_14 pin. The Comparator is connected to a P0_13 pin. The Counter is connected to a P0_12 pin. The Comparator is connected to a P0_11 pin. The Counter is connected to a P0_10 pin. The Comparator is connected to a P0_9 pin. The Counter is connected to a P0_8 pin. The Comparator is connected to a P0_7 pin. The Counter is connected to a P0_6 pin. The Comparator is connected to a P0_5 pin. The Counter is connected to a P0_4 pin. The Comparator is connected to a P0_3 pin. The Counter is connected to a P0_2 pin. The Comparator is connected to a P0_1 pin. The Counter is connected to a P0_0 pin.

	Total	Used
Analog Blocks	12	7
Digital Blocks	8	8
RAM	256	6
ROM	16,384	674

Cyrix MicroSIS/ING

8-Bit Timer **Timer8** **Revision A**

Resources:	Required	Optional
PSoC™ Blocks	1 Digital, 0 Analog	
Memory	46 bytes FLASH, 0 bytes SRAM	SRAM in Default Interrupt Routine
Pins		1 per External I/O

Send User Module: Timers

Resources Overview Diagram Features Description Specs Options Restrictions API Sample Code Examples Release Notes

For Help, press F1

Start | newproject - P... | Document1 - M...

3:02 PM

Figure 7